



## High Efficient Surface Mount Rectifiers

Reverse Voltage 50 to 1000 Volts Forward Current 1.0 Ampere

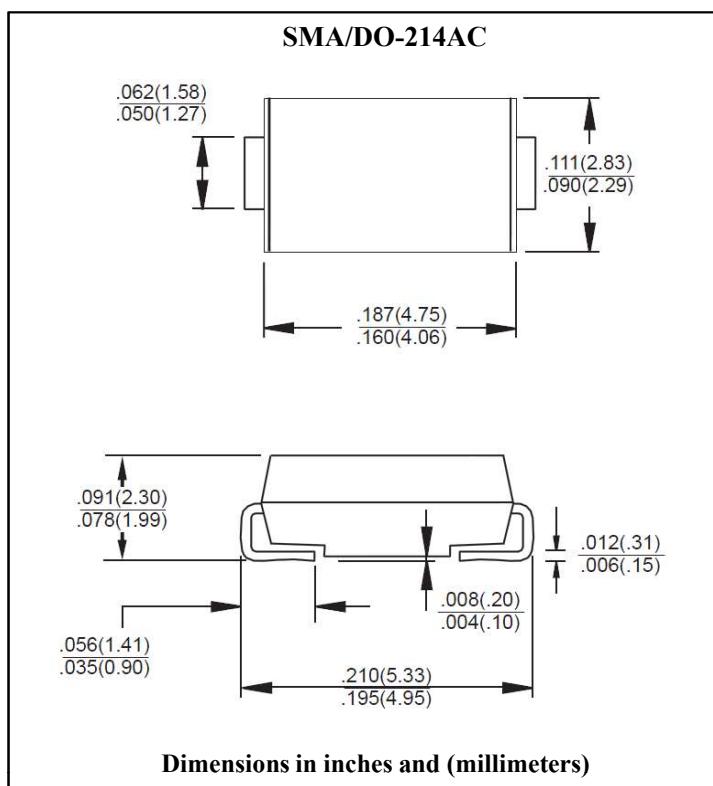
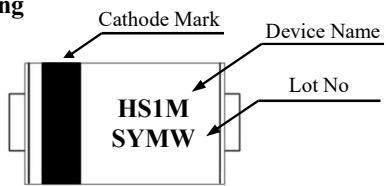
### Features

- For surface mounted application
- Glass passivated junction chip
- Low forward voltage drop
- Low profile package
- Built-in strain relief, ideal for automatic placement
- Fast switching for high efficiency
- Plastic material used carries underwriters laboratory classification 94V-O
- High temperature soldering : 260°C /10 seconds at terminals

### Mechanical Data

- Case : Molded plastic
- Terminals : Solder plated
- Polarity : Indicated by cathode band
- Packaging : 12mm tape per EIA STD RS-481
- Weight : 0.064gram

### Marking



### Maximum Ratings & Electrical Characteristics

Ratings at 25°C ambient temperature unless otherwise specified

Single phase half wave 60 HZ, resistive or inductive load

For capacitive load, derate current by 20%

Parameter	Symbol	HS1A	HS1B	HS1D	HS1G	HS1J	HS1K	HS1M	Unit	Remark					
Maximum Repetitive Peak Reverse Voltage	V <sub>RRM</sub>	50	100	200	400	600	800	1000	V						
Maximum RMS Voltage	V <sub>RMS</sub>	35	70	140	280	420	560	700	V						
Maximum DC Blocking Voltage	V <sub>DC</sub>	50	100	200	400	600	800	1000	V						
Maximum Average Forward Rectified Current See Fig.1	I <sub>F(AV)</sub>	1.0						A							
Peak Forward Surge Current 8.3 ms Single Half Sine-wave Superimposed on Rated Load (JEDEC Method)	I <sub>FSM</sub>	30						A							
Maximum Instantaneous Forward Voltage @ 1.0A	V <sub>F</sub>	1.0		1.3		1.7		V							
Maximum DC Reverse Current at Rated DC Blocking Voltage	I <sub>R</sub>	5.0						uA	Ta=25°C						
		100						uA	Ta=100°C						
Maximum Reverse Recovery Time	trr	50			75			ns	Note 1						
Typical Junction Capacitance	C <sub>J</sub>	20			15			pF	Note 2						
Typical Thermal Resistance	R <sub>th(j-a)</sub>	70						°C/W	Note 3						
Operation Junction Temperature Range	T <sub>J</sub>	-55 to +150						°C							
Storage Temperature Range	T <sub>STG</sub>	-55 to +150						°C							

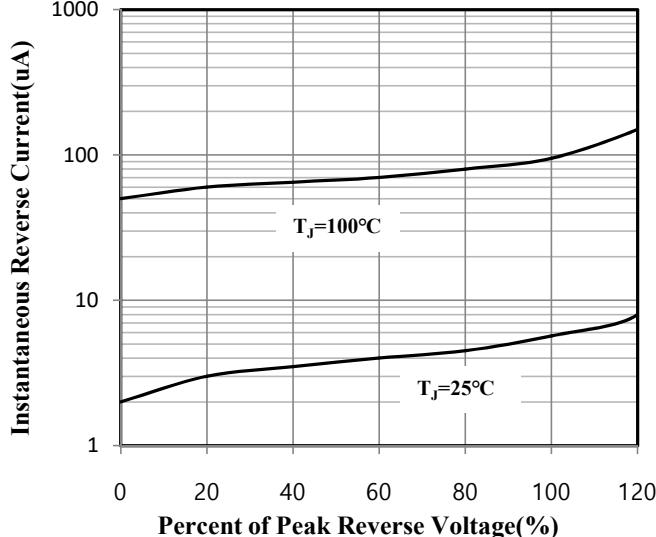
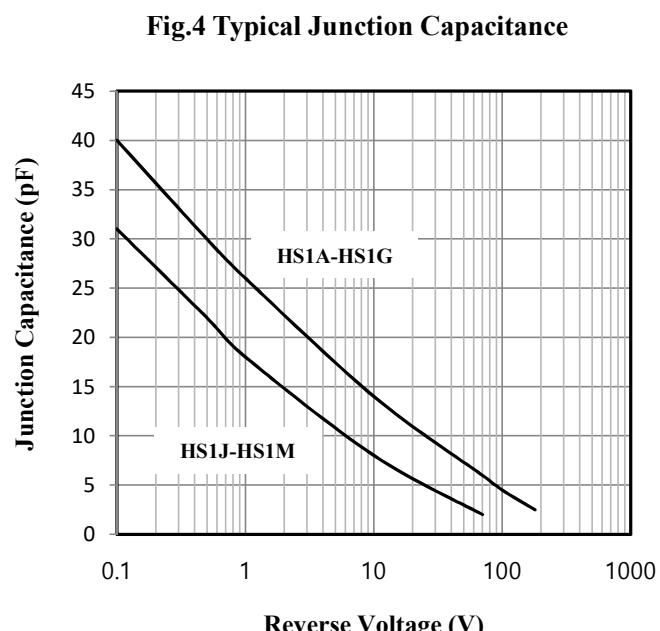
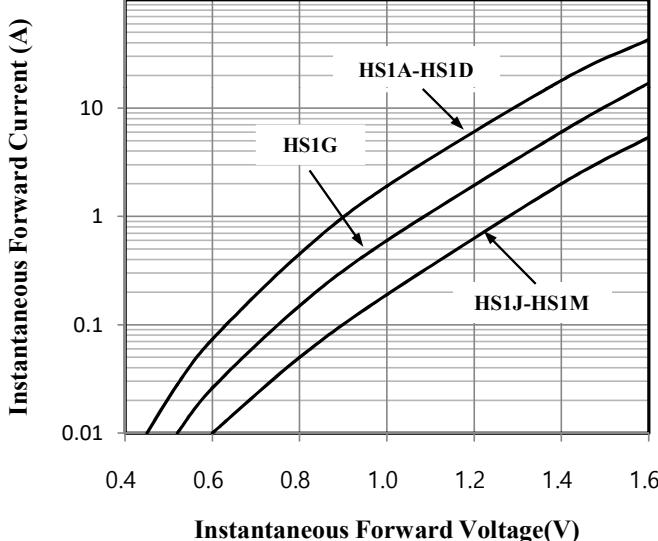
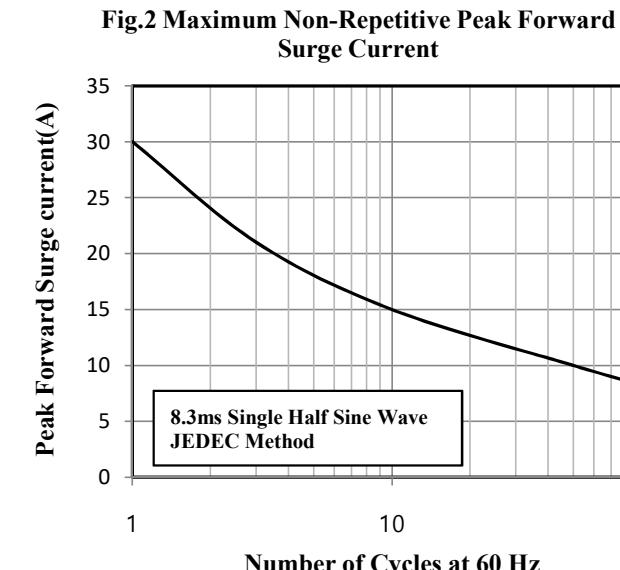
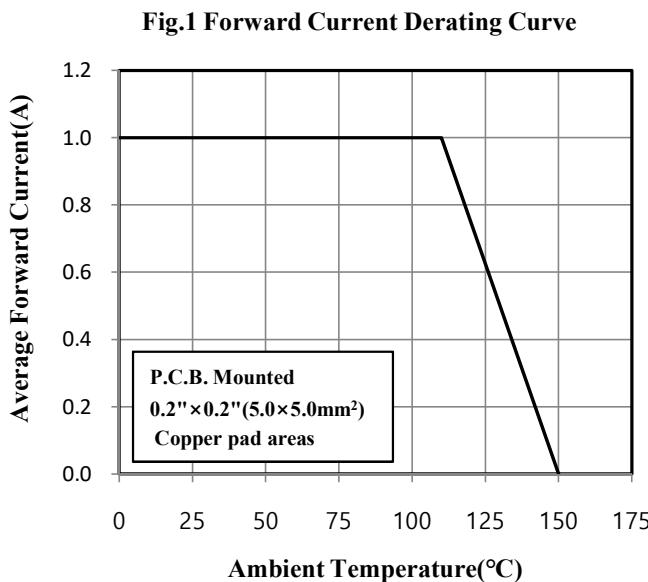
Note 1. Reverse Recovery Time Test Conditions : I<sub>F</sub>=0.5A, I<sub>R</sub>=1.0A, I<sub>RR</sub>=0.25A

Note 2. Measured at 1MHz and Applied Reverse Voltage of 4.0Volts D.C.

Note 3. Mounted on P.C.B with 0.2"×0.2" (5mm×5mm) Copper Pad Areas



**Ratings and Characteristics Curves (Ta=25°C unless otherwise noted)**



**Fig.6 Reverse Recovery Time Charateristic and Test Circuit Diagram**

